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4-11-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

**Yoshiaki FUKUZUMI et al.**

Serial No.: 09/783,023

Filed: February 15, 2001

For: SEMICONDUCTOR DEVICE USING  
FUSE/ANTI-FUSE SYSTEM AND METHOD  
OF MANUFACTURING THE SAME

Atty. Docket No.: 001701.00059

Group Art Unit: 2823

Examiner: G. Fourson, III

Confirmation No.: 6061

**RESPONSE TO ELECTION OF SPECIES REQUIREMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

This paper is responsive to the office action mailed March 13, 2003 in connection with the above-identified patent application and is filed prior to the expiration of the one (1) month period for response set therein.

In response, Applicants elect, without traverse, the first species including forming an isolation trench in combination with a conductive film on a gate dielectric in a different recess, as shown in figures 1-21, corresponding to claims 1-11.


No fee is believed to be associated with the filing of this paper. Nonetheless, should the U.S. Patent and Trademark Office determine that a fee is required, authorization is given to charge our Deposit Account No. 19-0733.

Respectfully submitted,

BANNER & WITCOFF, LTD.

Dated: April 8, 2003

By:

  
Gary D. Fedorochko  
Registration No. 35,509

1001 G Street, N.W.  
Washington, D.C. 20001-4597  
(202) 508-9100

GDF:lab

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